

DISC TYPE THYRISTOR**Features**

- Metal case with ceramic insulator
- All diffused amplifying gate
- High surge current capabilities
- Guaranteed high dv/dt
- Guaranteed high di/dt
- Low thermal impedance

990A**Typical Applications**

- DC Motor Control
- AC Controllers

Major Ratings and Characteristics

Parameters		T990A 1K8V	Units
$I_{T(AV)}$		900	A
	@ T_{hs}	85	°C
$I_{T(RMS)}$		1440	A
	@ T_{hs}	25	°C
I_{TSM}	@ 50Hz	15000	A
	@ 60Hz	16000	A
I^2t	@ 50Hz	110	KA ² s
	@ 60Hz	100	KA ² s
V_{DRM} / V_{RRM}		1800	V
T_q	typical	200	μs
T_J	range	- 40 to 125	°C

PHD**T990A 1K8V...SERIES****ELECTRICAL SPECIFICATIONS****Voltage Ratings**

Type number	Voltage Code	V_{RRM} , maximum repetitive peak reverse voltage V	V_{RSM} , maximum non- repetitive peak rev. voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_J$ max. mA
T990A	18	1800	1900	40
	20	2000	2100	
	22	2200	2300	
	26	2600	2700	

On-state Conduction

Parameter	T990A	Units	Conditions
$I_{T(AV)}$ Maximum average on-state current @ Heatsink temperature	990	A	180° conduction, half sine wave
	85	°C	Double side(single side)cooled
$I_{(RMS)}$ Maximum RMS on-state current	1440	A	DC@ 25°C heatsink temperature double side cooled
I_{TSM} Maximum peak, one-cycle non-repetitive surge current	15050	A	t = 10ms No voltage
	16100		t = 8.3ms reapplied
	17100		t = 10ms 100% V_{RRM}
	18200		t = 8.3ms reapplied
$I^2 t$ Maximum $I^2 t$ for fusing	110	$KA^2 s$	t = 10ms No voltage
	100		t = 8.3ms reapplied
	77		t = 10ms 100% V_{RRM}
	71		t = 8.3ms reapplied
$I^2 \sqrt{t}$ Maximum $I^2 \sqrt{t}$ for fusing	1100	$KA^2 \sqrt{s}$	t = 0.1 to 10ms, no voltage reapplied
V_{TM} Max. peak on-state voltage	2.07	V	$I_{TM} = 1255A$, $T_J = T_J$ max, t p = 10ms sine wave pulse
I_H Maximum holding current	600	mA	$T_J = 25^\circ C$, $I_T > 30A$
I_L Typical latching current	1000		$T_J = 25^\circ C$, $V_A = 12V$, $R_a = 6\Omega$, $I_G = 1A$

Switching

Parameter	T990A	Units	Conditions
di/dt Maximum non repetitive rate of rise of turned-on current	1000	A/ μs	$T_J = T_J$ max, $V_{DRM} = \text{rated } V_{DRM}$ $I_{TM} = 2 \times di/dt$
t_d Typical delay time	1.1	μs	$T_J = 25^\circ C$, $V_{DM} = \text{rated } V_{DRM}$, $I_{TM} = 50A$ DC, $t_p = 1\mu s$ Resistive load Gate pulse: 10V, 5 Ω source
T_q Max. turn-off time (*)	Min 200		$T_J = T_J$ max, $I_{TM} = 300A$, commutating $di/dt = 20A/\mu s$, $V_R = 50V$, $t_p = 500\mu s$ dv/dt :see table in device code

Blocking

Parameter	T990A	Units	Conditions
dv/dt Maximum critical rate of rise of off-state voltage	500	V/ μ s	T _J = T _J max. linear to 80% V _{DRM} , higher value available on request
I _{DRM} Max. peak reverse and off-state leakage current	40	mA	T _J = T _J max, rated V _{DRM} /V _{RRM} applied

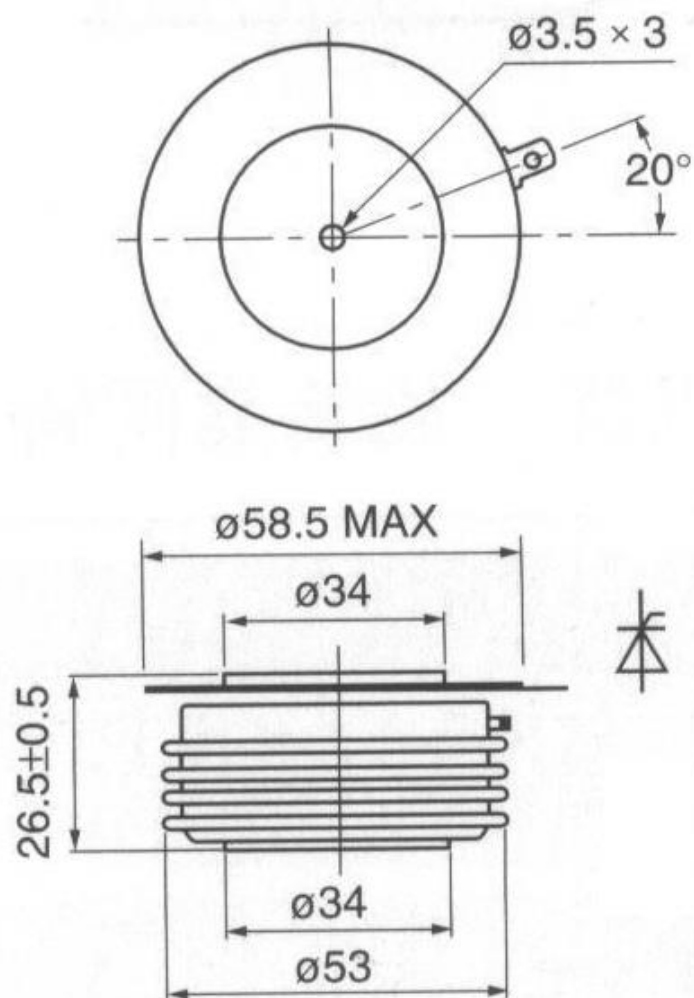
Triggering

Parameter	T990A	Units	Conditions
P _{GM} Maximum peak gate power	60	W	T _J = T _J max, f = 50Hz, d% = 50
P _{G(AV)} Maximum average gate power	10		
I _{GM} Max. peak positive gate current	10	A	T _J = T _J max, t _p ≤ 5ms
+V _{GM} Maximum peak positive gate voltage	20	V	T _J = T _J max, t _p ≤ 5ms
-V _{GM} Maximum peak negative gate voltage	5.0		
I _{GT} DC gate current required to trigger	200	mA	T _J = 25°C, V _A = 12V, R _a = 6Ω
V _{GT} DC gate voltage required to trigger	3	V	
I _{GD} DC gate current not to trigger	20	mA	T _J = T _J max, rated V _{DRM} applied
V _{GD} DC gate voltage not to trigger	0.25	V	

Thermal and Mechanical Specification

Parameter	T990A	Units	Conditions
T _J Max. operating temperature range	-40 to 125	°C	
T _{stg} Max. storage temperature range	-40 to 150		
R _{thJ-hs} Max. thermal resistance, junction to case	0.17 0.08	K/W	DC operation single side cooled DC operation double side cooled
R _{thC-hs} Max. thermal resistance, case to heatsink	0.033 0.017		DC operation single side cooled DC operation double side cooled
T Mounting torque, ± 10%	4900	N	
wt Approximate weight	330	g	

Outline Table



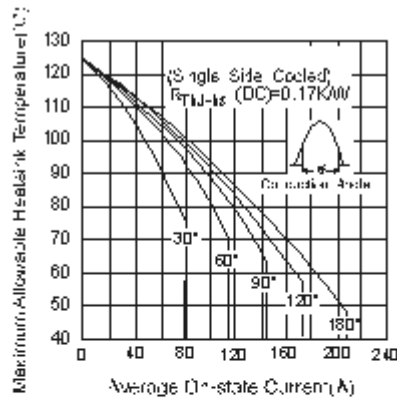


Fig.1-Current Ratings Characteristics

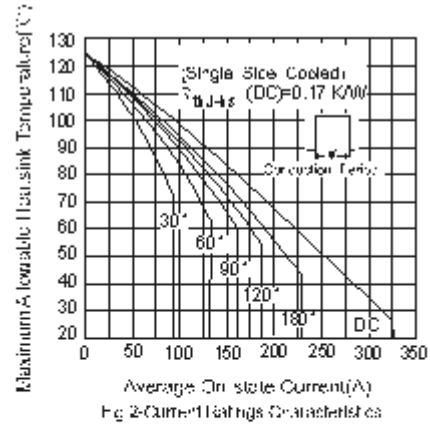


Fig.2-Current Ratings Characteristics

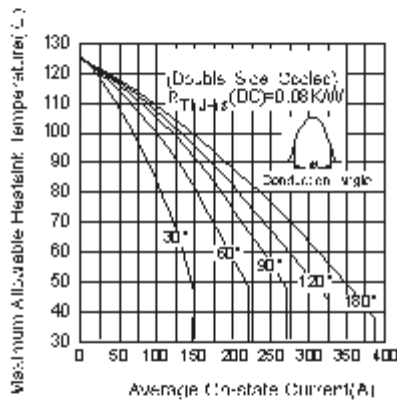


Fig.3-Current Ratings Characteristics

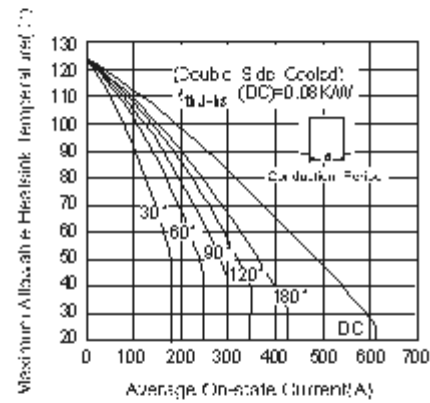


Fig.4-Current Ratings Characteristics

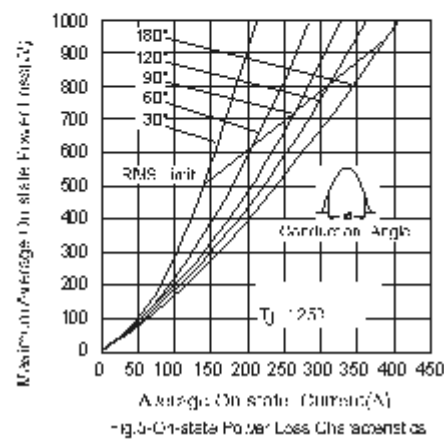


Fig.5-On-state Power Loss Characteristics

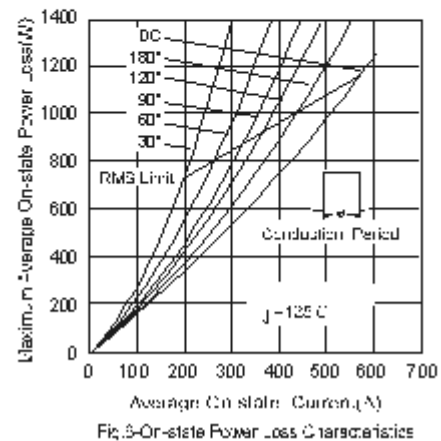


Fig.6-On-state Power Loss Characteristics

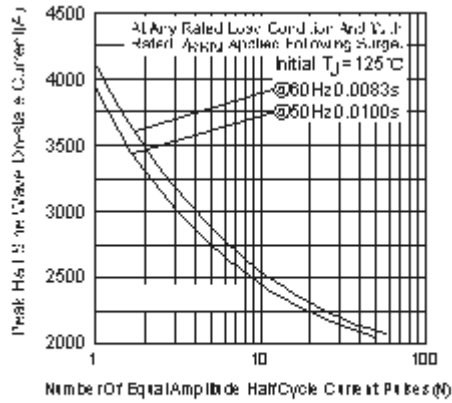


Fig.7-Maximum Non-Repetitive Surge Current Single and Double Side Cooled

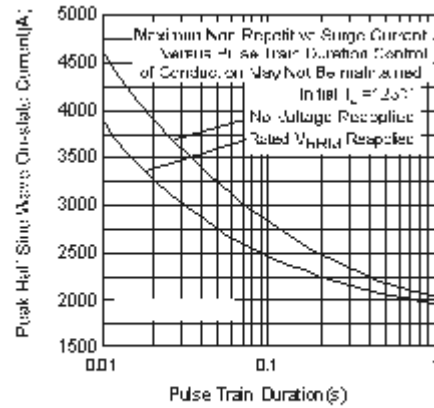


Fig.8-Maximum Non-Repetitive Surge Current Single and Double Side Cooled

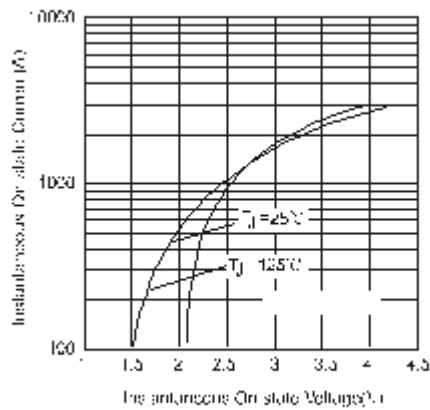


Fig.9-On-state Voltage Drop Characteristics

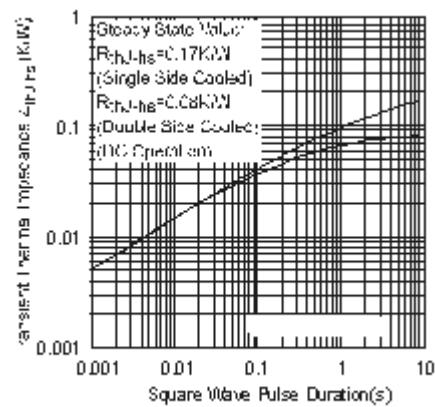


Fig.10-Thermal Impedance Z_{thJC} Characteristics